



NPN Low VCEsat Transistor

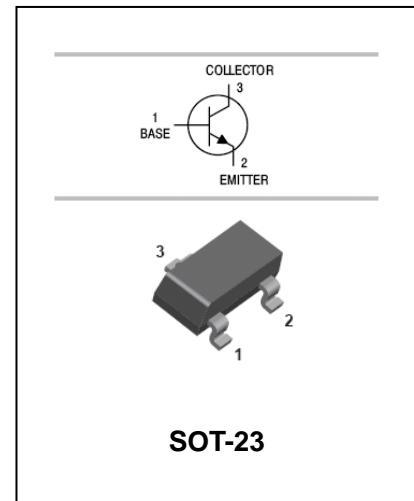
PBSS4160T

FEATURES

- Low collector-emitter saturation voltage VCEsat
- High collector current capability IC and ICM
- High efficiency, reduces heat generation
- Reduces printed-circuit board area required



Lead-free



APPLICATIONS

- Major application segments
- Power management
- Peripheral driver

ORDERING INFORMATION

| Type No. | Marking | Package Code |
|-----------|---------|--------------|
| PBSS4160T | U5 | SOT-23 |

MAXIMUM RATING @ Ta=25°C unless otherwise specified

| Symbol | Parameter | Value | Units |
|-------------------------------------|--|----------------------------|---------------|
| V _{CBO} | Collector-Base Voltage | 80 | V |
| V _{CEO} | Collector-Emitter Voltage | 60 | V |
| V _{EBO} | Emitter-Base Voltage | 5 | V |
| I _C | Collector Current -Continuous | 0.9 (note 1) 1 (note 2) | A |
| I _{CM} | peak collector current (t = 1 ms or limited by T _{j(max)}) | 2 | A |
| I _B | base current (DC) | 300 | mA |
| I _{BM} | peak base current (tp ≤ 300 ms; δ ≤ 0.02) | 1 | A |
| P _C | Collector Dissipation T _{amb} ≤ 25 °C; note 1 T _{amb} ≤ 25 °C; note 2 T _{amb} ≤ 25 °C; notes 1 and 3 | 270 400 1.25 | mW mW W |
| T _{stg} , T _{amb} | Storage and operating ambient temperature | -65 to +150 | °C |
| T _j | Junction temperature | 150 | °C |

Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, tin-plated, standard



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footprint.

2. Device mounted on an FR4 printed-circuit board, single-sided copper, tin-plated, 1 cm² collector mounting pad.
3. Operated under pulsed conditions: duty cycle $\delta \leq 20\%$, pulse width $t_p \leq 10$ ms.

ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

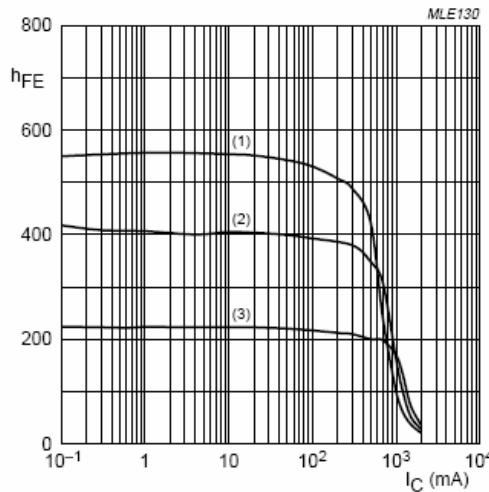
| Parameter | Symbol | Test conditions | MIN | TYP | MAX | UNIT |
|--------------------------------------|----------------------|---|-----|---------------------|----------------------|------|
| Collector-base breakdown voltage | V _{(BR)CBO} | I _C =100µA,I _E =0 | 80 | | | V |
| Collector-emitter breakdown voltage | V _{(BR)CEO} | I _C =10mA,I _B =0 | 60 | | | V |
| Emitter-base breakdown voltage | V _{(BR)EBO} | I _E =100µA,I _C =0 | 5 | | | V |
| Collector cut-off current | I _{CBO} | V _{CB} =60V,I _E =0 | | | 0.1 | µA |
| collector-emitter cut-off current | I _{CES} | V _{CE} =60V,I _E =0 | | | 0.1 | µA |
| Emitter cut-off current | I _{EBO} | V _{EB} =5V,I _C =0 | | | 0.1 | µA |
| DC current gain | h _{FE} | V _{CE} =5V,I _C =500mA | 200 | 350 | | |
| | | V _{CE} =5V,I _C =1A | 100 | 150 | | |
| Collector-emitter saturation voltage | V _{CE(sat)} | I _C =500mA, I _B = 50mA I _C =1A, I _B = 100mA I _C =100mA, I _B = 1mA | | 0.11 0.2 0.09 | 0.14 0.25 0.11 | V |
| Base-emitter saturation voltage | V _{BE(sat)} | I _C =1A, I _B = 50mA | | 0.95 | 1.1 | V |
| Base-emitter voltage | V _{BE(on)} | I _C =1A,V _{CE} =5V | | 0.82 | 0.9 | V |
| Transition frequency | f _T | V _{CE} =10V, I _C = 50mA f=100MHz | 150 | 220 | | MHz |
| Collector output capacitance | C _{ob} | V _{CB} =10V,f=1MHz | | 5.5 | 10 | pF |



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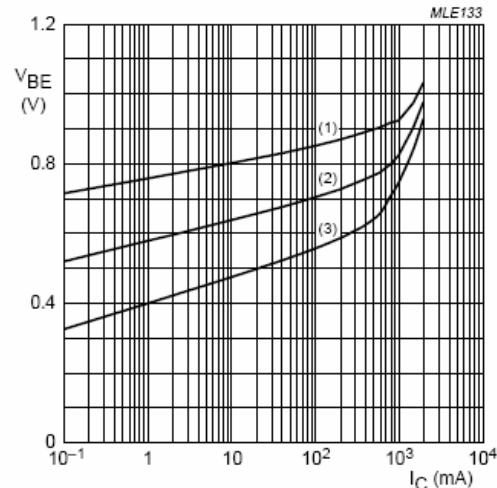
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TYPICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified



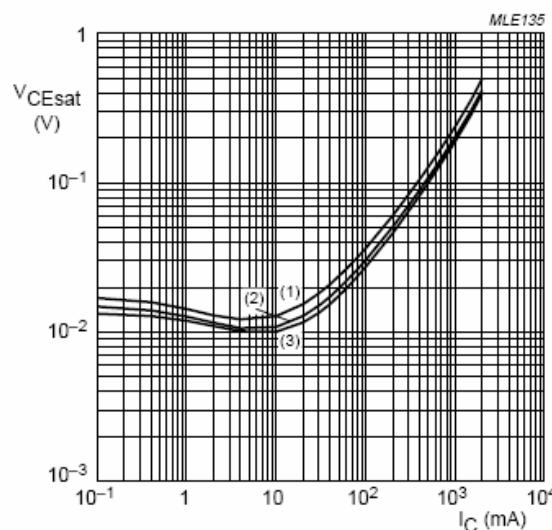
$V_{CE} = 5$ V.
(1) $T_{amb} = 100^\circ C$.
(2) $T_{amb} = 25^\circ C$.
(3) $T_{amb} = -55^\circ C$.

Fig.1 DC current gain as a function of collector current; typical values.



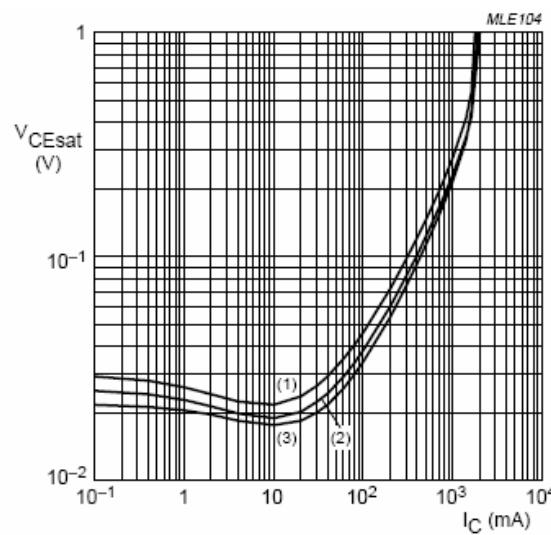
$V_{CE} = 5$ V.
(1) $T_{amb} = -55^\circ C$.
(2) $T_{amb} = 25^\circ C$.
(3) $T_{amb} = 100^\circ C$.

Fig.2 Base-emitter voltage as a function of collector current; typical values.



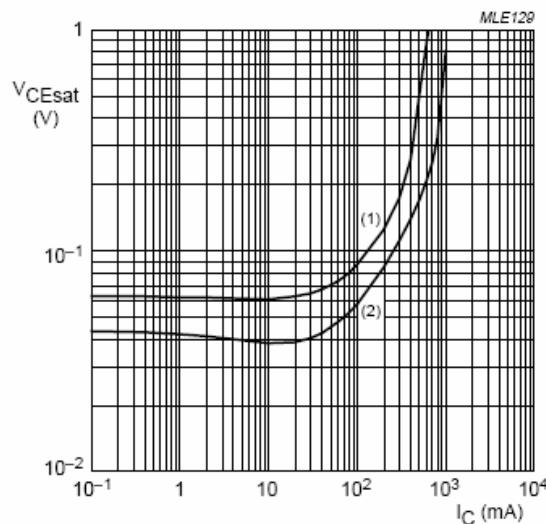
$I_C/I_B = 10$.
(1) $T_{amb} = 100^\circ C$.
(2) $T_{amb} = 25^\circ C$.
(3) $T_{amb} = -55^\circ C$.

Fig.3 Collector-emitter saturation voltage as a function of collector current; typical values.



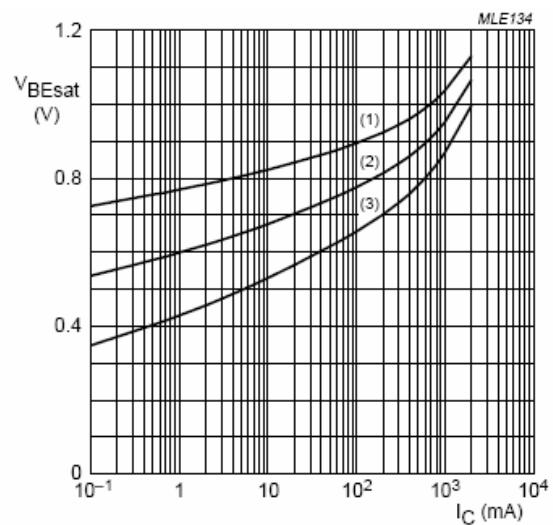
$I_C/I_B = 20$.
(1) $T_{amb} = 100^\circ C$.
(2) $T_{amb} = 25^\circ C$.
(3) $T_{amb} = -55^\circ C$.

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.

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 $T_{amb} = 25^\circ\text{C}$.

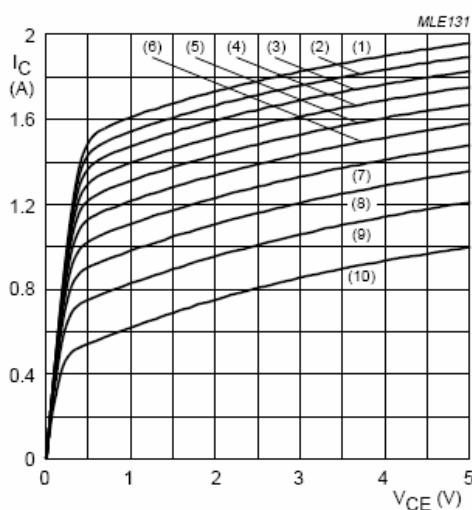
- (1)
- $I_C/I_B = 100$
- .
-
- (2)
- $I_C/I_B = 50$
- .

Fig. 5 Collector-emitter saturation voltage as a function of collector current; typical values.


 $I_C/I_B = 20$.

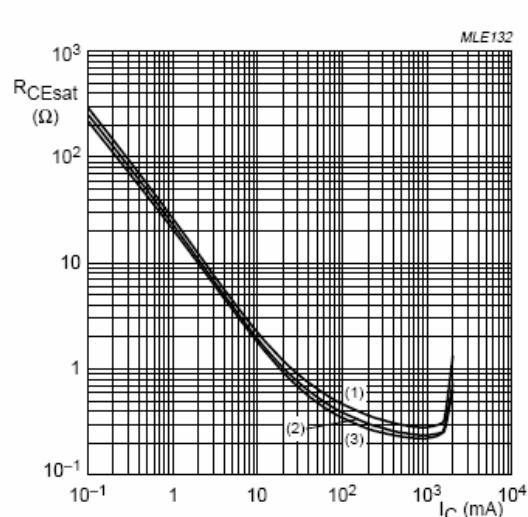
- (1)
- $T_{amb} = -55^\circ\text{C}$
- .
-
- (2)
- $T_{amb} = 25^\circ\text{C}$
- .
-
- (3)
- $T_{amb} = 100^\circ\text{C}$
- .

Fig. 6 Base-emitter saturation voltage as a function of collector current; typical values.


 $T_{amb} = 25^\circ\text{C}$.

- (1)
- $I_B = 60$
- mA.
-
- (2)
- $I_B = 54$
- mA.
-
- (3)
- $I_B = 48$
- mA.
-
- (4)
- $I_B = 42$
- mA.
-
- (5)
- $I_B = 36$
- mA.
-
- (6)
- $I_B = 30$
- mA.
-
- (7)
- $I_B = 24$
- mA.
-
- (8)
- $I_B = 18$
- mA.
-
- (9)
- $I_B = 12$
- mA.
-
- (10)
- $I_B = 6$
- mA.

Fig. 7 Collector current as a function of collector-emitter voltage; typical values.


 $I_C/I_B = 20$.

- (1)
- $T_{amb} = 100^\circ\text{C}$
- .
-
- (2)
- $T_{amb} = 25^\circ\text{C}$
- .
-
- (3)
- $T_{amb} = -55^\circ\text{C}$
- .

Fig. 8 Equivalent on-resistance as a function of collector current; typical values.



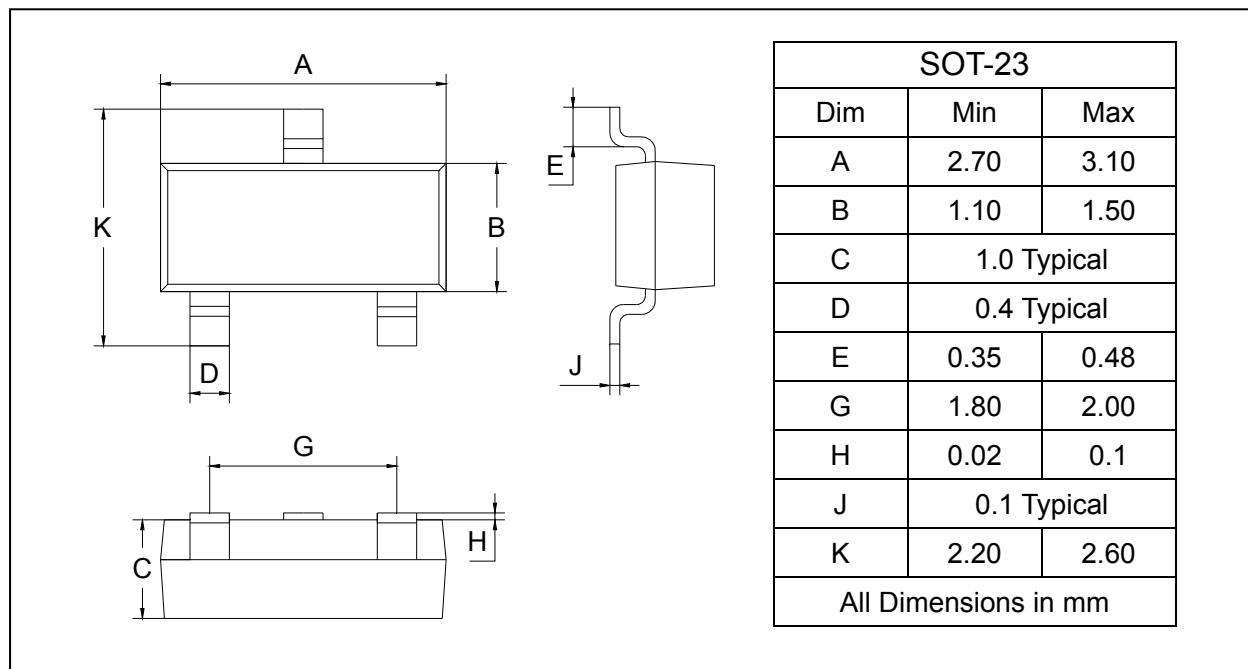
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PACKAGE OUTLINE

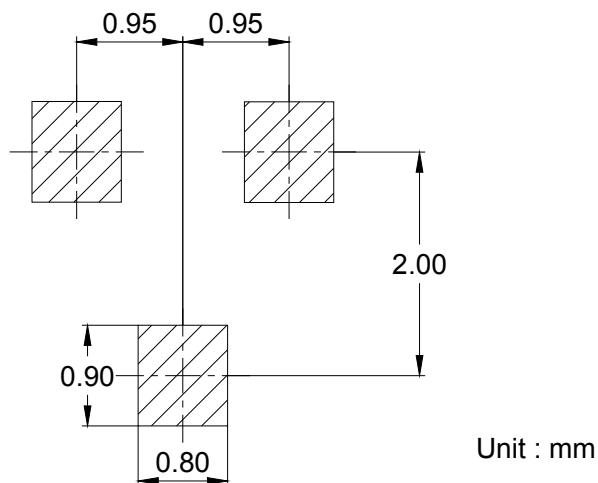
Plastic surface mounted package

SOT-23



| SOT-23 | | |
|----------------------|-------------|------|
| Dim | Min | Max |
| A | 2.70 | 3.10 |
| B | 1.10 | 1.50 |
| C | 1.0 Typical | |
| D | 0.4 Typical | |
| E | 0.35 | 0.48 |
| G | 1.80 | 2.00 |
| H | 0.02 | 0.1 |
| J | 0.1 Typical | |
| K | 2.20 | 2.60 |
| All Dimensions in mm | | |

SOLDERING FOOTPRINT



PACKAGE INFORMATION

| Device | Package | Shipping |
|-----------|---------|----------------|
| PBSS4160T | SOT-23 | 3000/Tape&Reel |